

Micro Commercial Components 20736 Marilla Street Chatsworth

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DTA114EUA

Features

- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0 and MSL Rating 1
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy

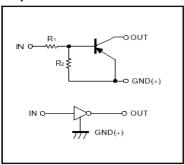
Absolute maximum ratings @ 25°

Symbol	Parameter	Min	Тур	Max	Unit
V_{cc}	Supply voltage		-50		V
V_{IN}	Input voltage	-40		10	V
I _O I _{C(MAX)}	Output current		-50 -100		mA
P_d	Power dissipation		200		mW
T_j	Junction temperature		150		$^{\circ}$
T _{stg}	Storage temperature	-55		150	$^{\circ}$

Electrical Characteristics @ 25°€

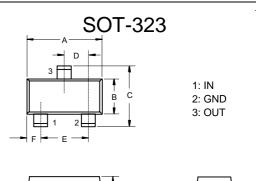
Symbol	Parameter	Min	Тур	Max	Unit
$V_{I(off)}$	Input voltage (V _{CC} =-5V, I _O =-100 μ A)		1	-0.5	V
$V_{I(on)}$	$(V_0=-0.3V, I_0=-10mA)$	-3.0			V
$V_{O(on)}$	Output voltage (I _O /I _I =-10mA/-0.5mA		-	-0.3	V
l _l	Input current (V _I =-5V)		-	-0.88	mA
$I_{O(off)}$	Output current (V _{CC} =-50V, V _I =0)			-0.5	μА
Gı	DC current gain (V ₀ =-5V, I ₀ =-5mA)	30			
R ₁	Input resistance	7.0	10	13	ΚΩ
R ₂ /R ₁	Resistance ratio	0.8	1.0	1.2	
f⊤	Transition frequency (V _{CE} =-10V, I _E =5mA, f=100MHz)		250		MHz

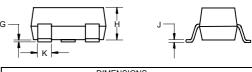
Equivalent circuit



MARKING: 14

PNP Digital Transistors





DIMENSIONS					
	INC	HES	MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.071	.087	1.80	2.20	
В	.045	.053	1.15	1.35	
C	.079	.087	2.00	2.20	
D	.026 Nominal		0.65Nominal		
ш	.047	.055	1.20	1.40	
F	.012	.016	.30	.40	
G	.000	.004	.000	.100	
Ι	.035	.039	.90	1.00	
J	.004	.010	.100	.250	
K	012	016	30	40	

Suggested Solder Pad Layout 0.70 1.90 0.65 0.65



Ordering Information

Device	Packing
(Part Number)-TP	Tape&Reel3Kpcs/Reel

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